Attorney's Docket No.: 07977-152003 / US3353D1D1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hisashi Ohtani Art Unit: Unknown Serial No.: New Divisional Application Examiner: Unknown

Filed: September 9, 2003

Title : SEMICONDUCTOR INTEGRATED CIRCUIT AND FABRICATION METHOD

THEREOF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Under 35 USC §120, this application relies on the earlier filing dates of application serial number 09/967,697, filed on September 28, 2001 and application serial number 08/862,895, filed on May 23, 1997. The attached list of references were submitted to and/or cited by the Office in the prior applications and, therefore, are not provided in this application.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: September 9, 2003

John F. Hayden Reg. No. 37,640

Reg. N

Fish & Richardson P.C. 1425 K Street, N.W. 11th Floor Washington, DC 20005-3500

Telephone: (202) 783-5070 Facsimile: (202) 783-2331

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next communication to applicant.

U.S. Department of Commerce Patent and Trademark Office Attorney's Docket No. 07977-152003

Application No. New Divisional Application

Information Disclosure Statement by Applicant (Use several sheets if necessary)

Applicant Hisashi Ohtani

Filing Date

Group Art Unit

(37 CFR §1.98(b))

September 9, 2003

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Examiner Initial	Desig.	Decument Number	Publication			0	Filing Date
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Examiner Signature	Date Considered
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ĺ	by Ap	ets if necessary)	Applicant Hisashi Ohtani	
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	AGG				
	АНН				
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Examiner Signature	Date Considered				
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